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PATENT  
Attorney's Doc. No. 4591-341

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent No.: US 6,873,016 B2  
Inventor: Park et al.  
Issued: March 29, 2005

Certificate  
JUN 29 2006

For: SEMICONDUCTOR DEVICE AND METHOD OF FORMING THE SAME

of Correction

Ser. No.: 10/678,586  
Group Art Unit: 2811

Filed: October 3, 2003  
Examiner: Thien F. Tran

TRANSMITTAL LETTER

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Enclosed for filing are the following:

- ☒ Request for Certificate of Correction
- ☒ Certificate of Correction Form PTO 1050 (in duplicate)
- ☒ Return postcard
- ☒ Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.

Alan T. McCollum  
Reg. No. 28,881

MARGER JOHNSON & McCOLLOM, P.C.  
210 SW Morrison Street, Suite 400  
Portland, OR 97204  
503-222-3613

Customer No. 20575

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Date: June 22, 2006

  
Zach Cooper

JUN 30 2006



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REQUEST FOR CERTIFICATE OF CORRECTION

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Alexandria, VA 22313-1450

Upon comparison of the above-identified original Letters Patent with applicant's copy of the application therefore, the following error in the original Letters Patent has been noted:

On Fig. 1, please replace "II" (located under RC(49)) with --II'--  
At column 5, line 1, please replace "insulation layer. 108" with --insulation layer 108.--  
At column 6, line 19, please replace "semiconductor device of FIG. referring to" with --semiconductor device of FIGS. 7 and 8 is explained by referring to--  
At column 8, lines 9-11, please replace claim 4 with --The semiconductor device as claimed in claim 1, wherein the conductive pattern and the resistor are horizontally aligned. --

Applicant respectfully requests that a Certificate of Correction calling attention to the above error be issued pursuant to Rule 322 of the Patent Office and Title 35, United States Code, Section 254. Two copies of a Certificate of Correction form are enclosed.

Respectfully submitted,

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PATENT NO : US 6,873,016 B2

DATED : March 29, 2005

INVENTOR(S) : Park et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On Fig. 1, please replace "II" (located under RC(49)) with --II'--

At column 5, line 1, please replace "insulation layer. 108" with --insulation layer 108.--

At column 6, line 19, please replace "semiconductor device of FIG. referring to" with --semiconductor device of FIGS. 7 and 8 is explained by referring to--

At column 8, lines 9-11, please replace claim 4 with --The semiconductor device as claimed in claim 1, wherein the conductive pattern and the resistor are horizontally aligned. --

MAILING ADDRESS OF SENDER:  
Marger Johnson & McCollom  
210 S.W. Morrison Street, Suite 400  
Portland, Oregon 97204

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